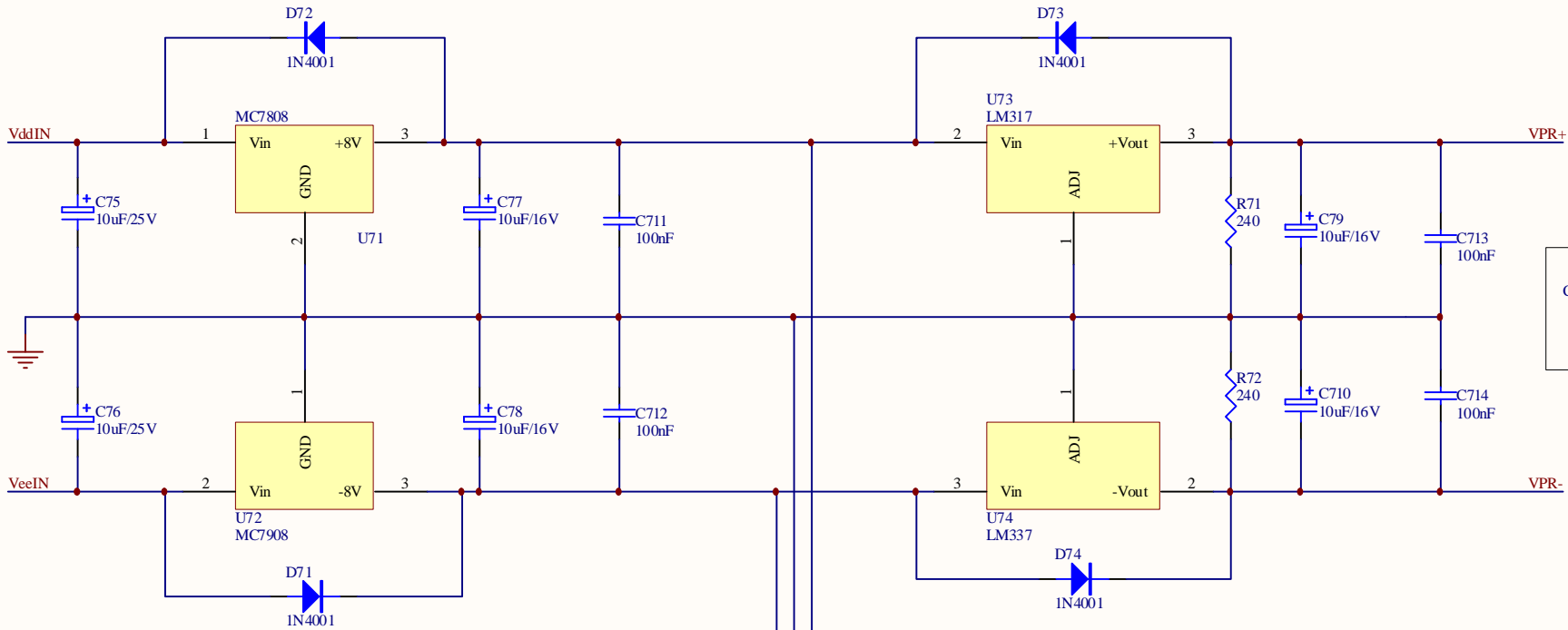
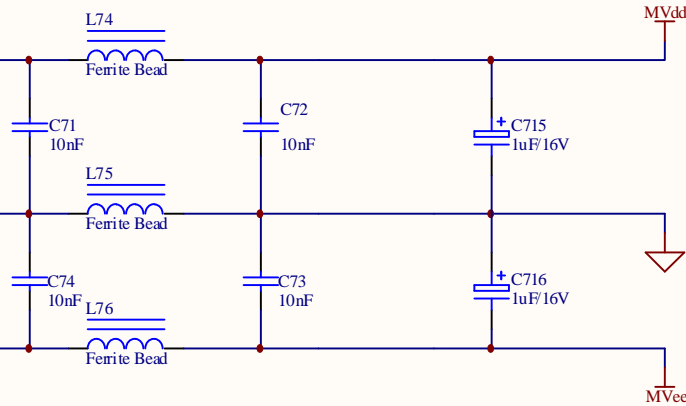


Title			
PYTHAGORAS: DSP 320C30 FOR THE AMIGA 3000			
Size	Number	Revision	
B	SCH-00-003002-ILX	V0.31	
Date:	19/11/2008	Sheet of	
File:	C:\temp\pyth2.pri	Drawn By:	ALEXOPOULOS ILIAS

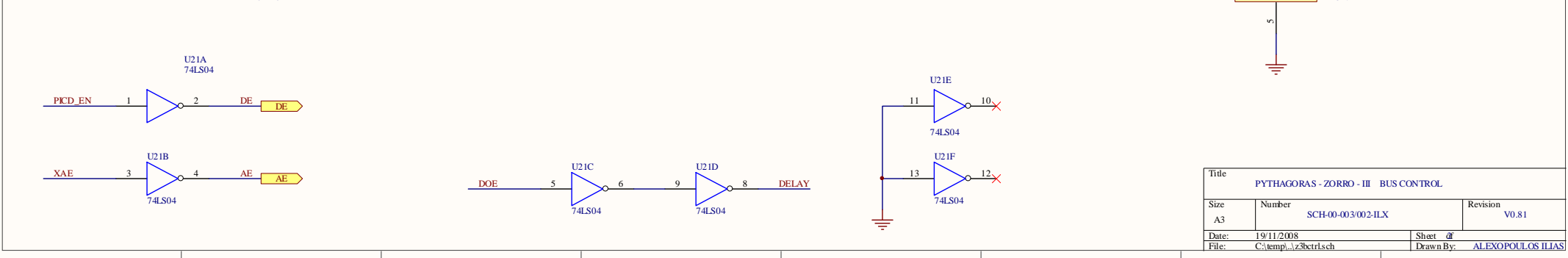
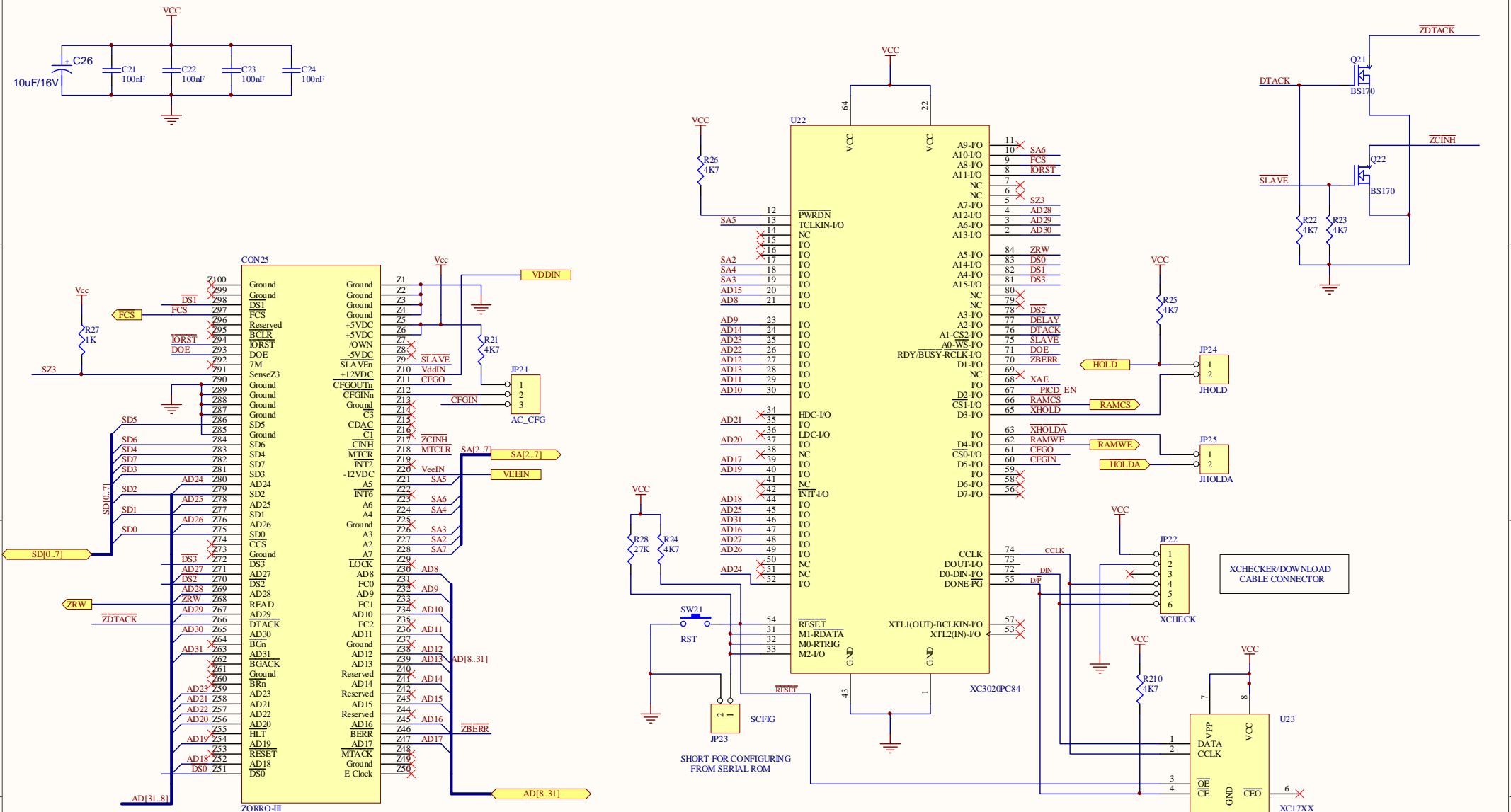


CODEC Input Protection Voltage
 +/-1.25V
 To Sheet #005

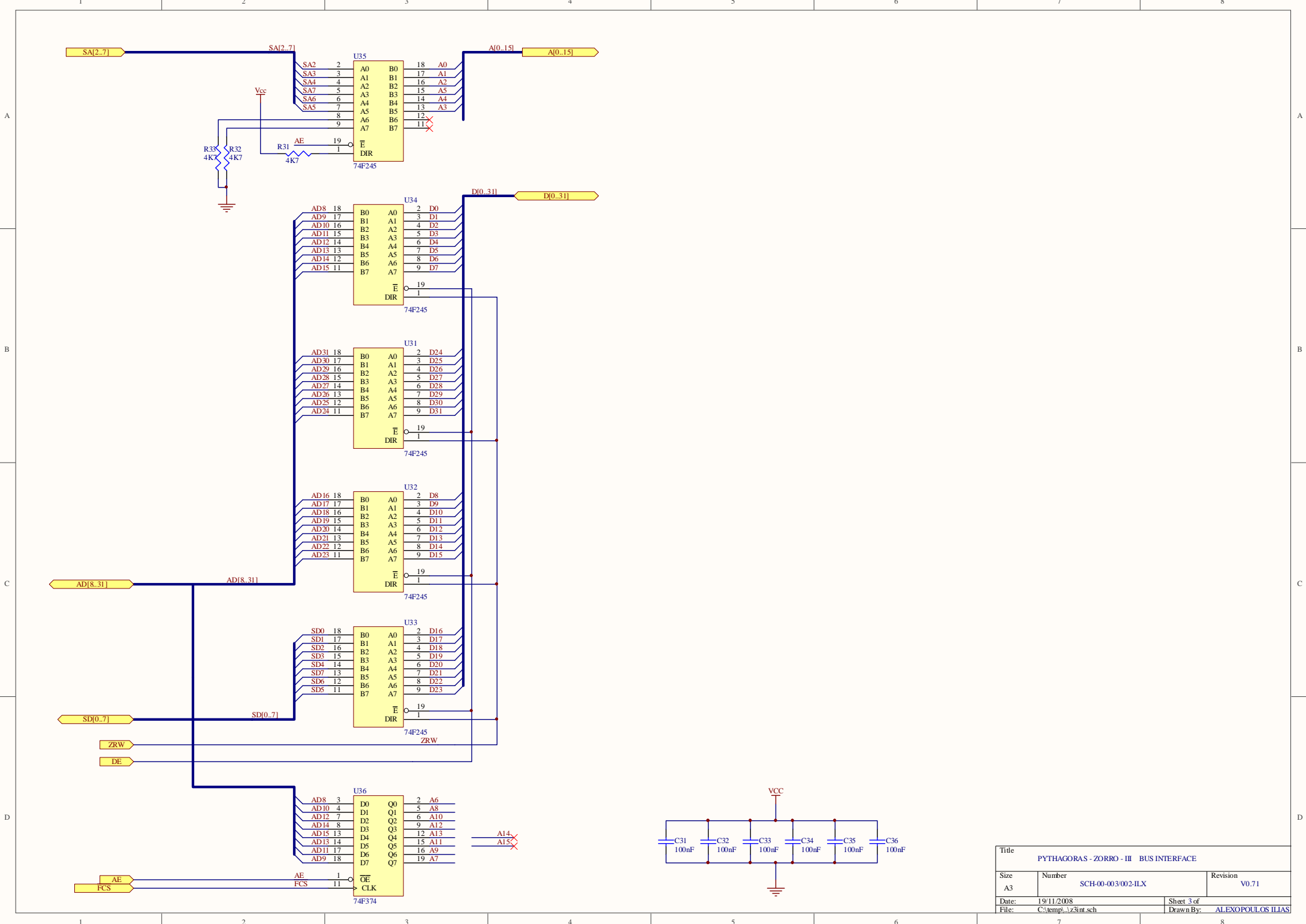
VDDIN=+12V
 VEEIN=-12V
 VDD=+8V
 VEE=-8V



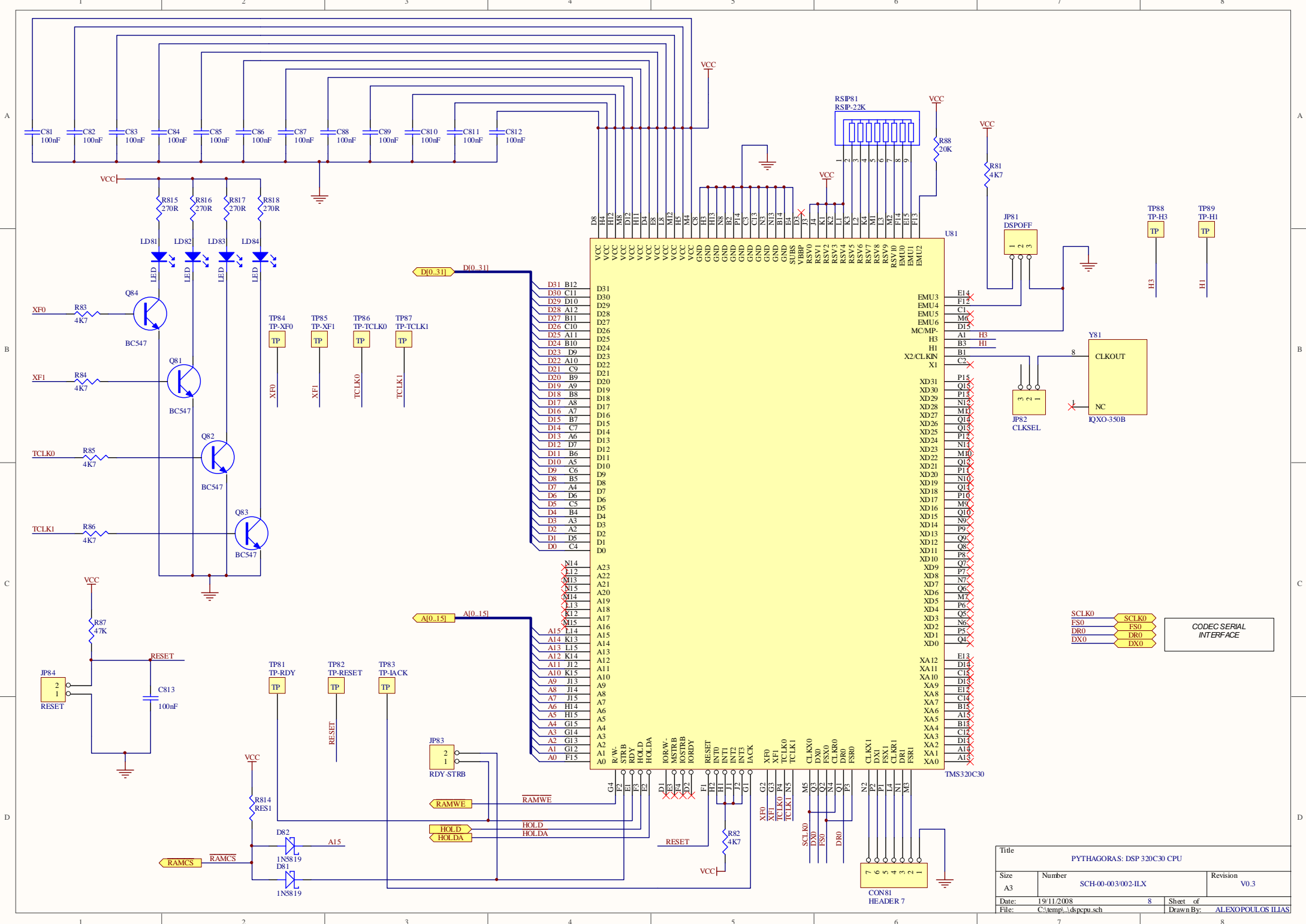
Title			PYTHAGORAS - Power Supply		
Size	Number	Revision			
A4	SCH-00-003/002-ILX	V0.21			
Date:	19/11/2008	Sheet	of		
File:	C:\temp\...\ps.sch	Drawn By:	ALEXOPOULOS ILIAS		



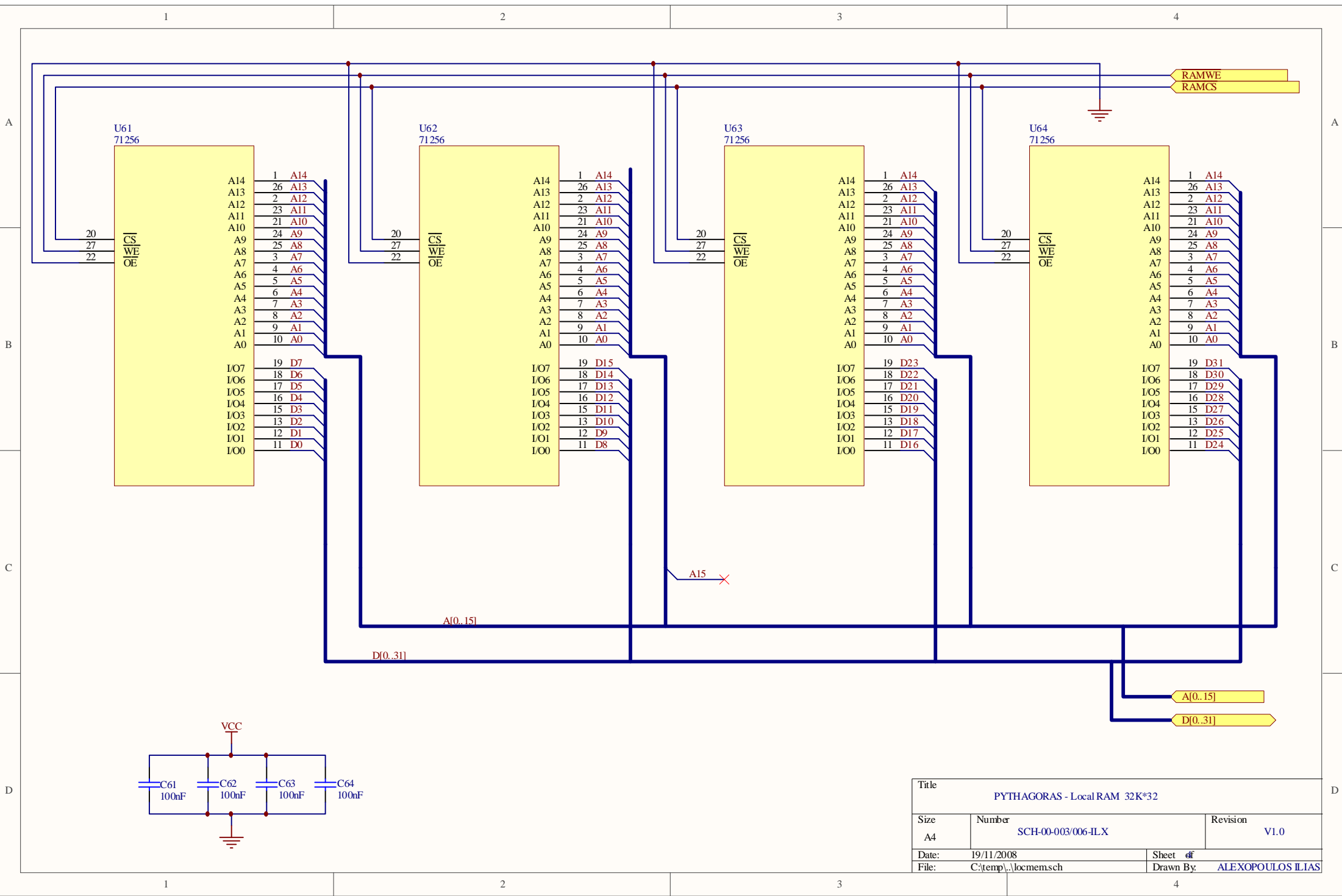
Title		
PYTHAGORAS - ZORRO - III BUS CONTROL		
Size	Number	Revision
A3	SCH-00-003/002-ILX	V0.81
Date:	19/11/2008	Sheet of
File:	C:\temp\z3bctrl.sch	Drawn By:
		ALEXOPOULOS ILLIAS



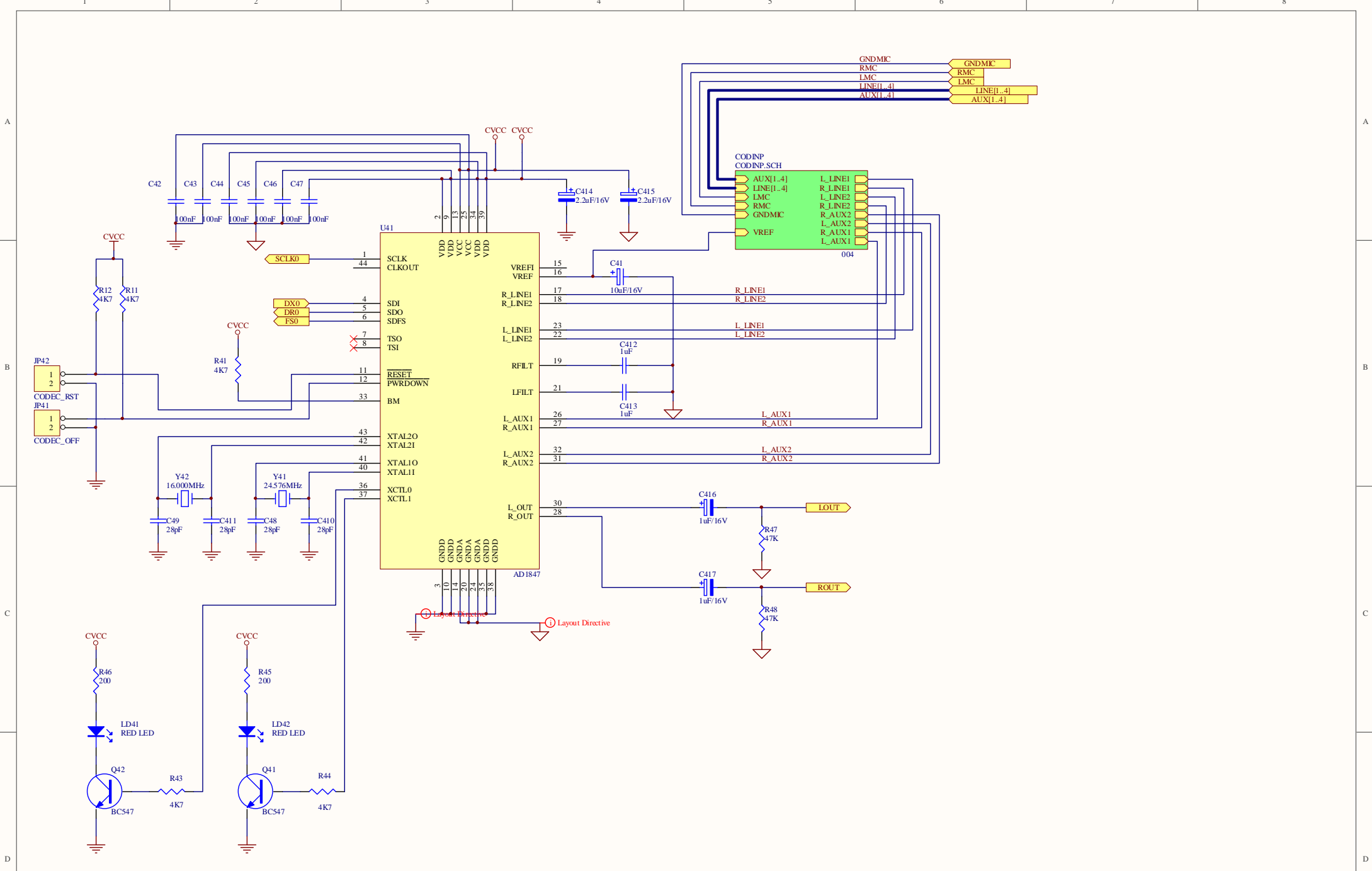
Title PYTHAGORAS - ZORRO - III BUS INTERFACE		
Size A3	Number SCH-00-003/002-ILX	Revision V0.71
Date: 19/11/2008	Sheet 3 of	Drawn By: ALEXOPOULOS ILLIAS
File: C:\temp\...z3int.sch		



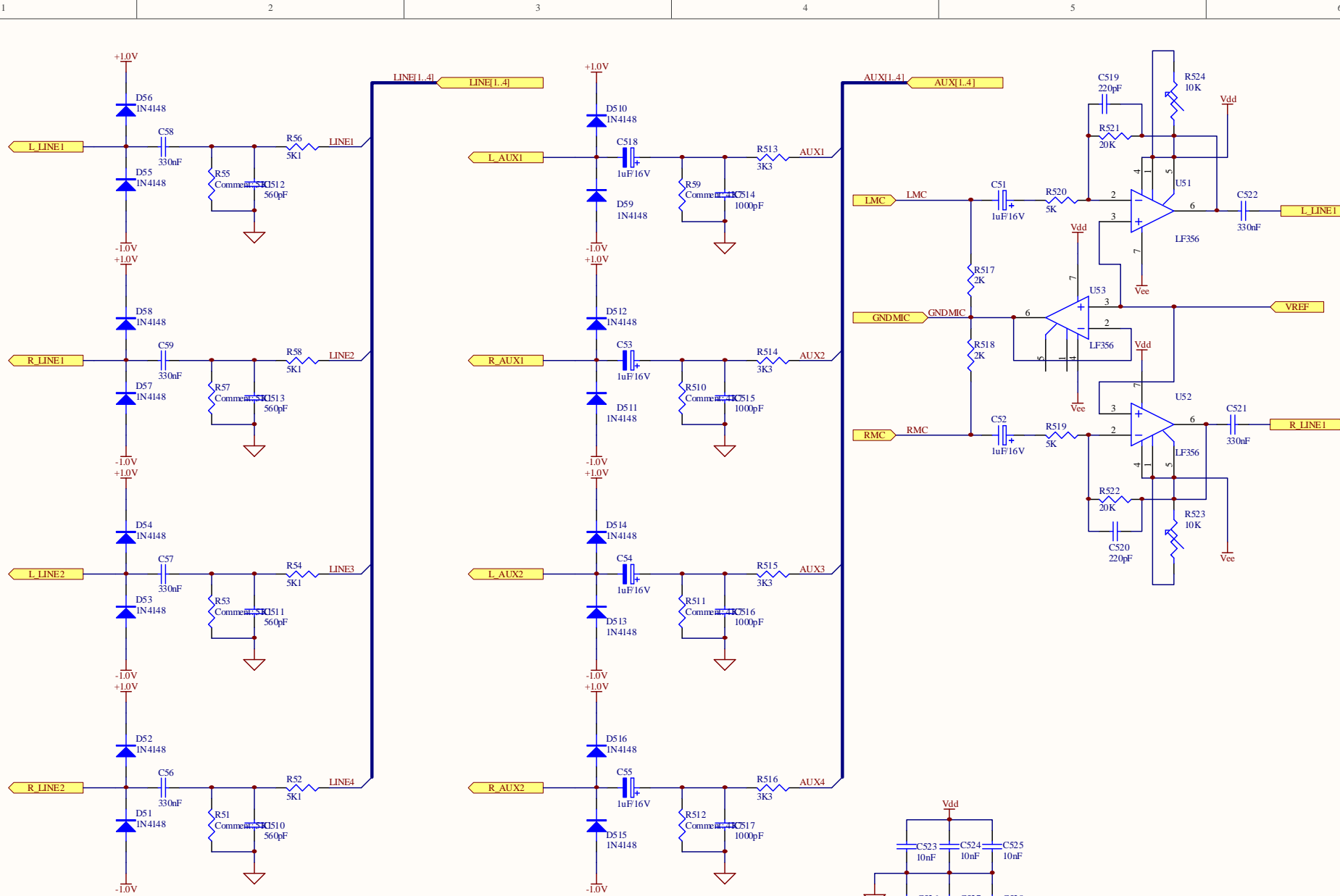
Title			
PYTHAGORAS: DSP 320C30 CPU			
Size	Number	Revision	
A3	SCH-00-003/002-ILX	V0.3	
Date:	19/11/2008	8	Sheet of
File:	C:\temp\dspepu.sch		Drawn By: ALEXOPOULOS ILLIAS



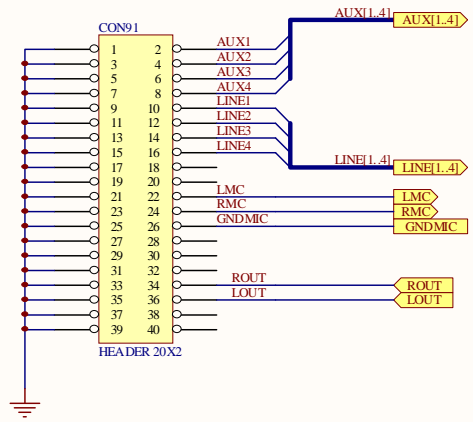
Title		
PYTHAGORAS - Local RAM 32K*32		
Size	Number	Revision
A4	SCH-00-003/006-ILX	V1.0
Date:	19/11/2008	Sheet of
File:	C:\temp\...\locmemsch	Drawn By: ALEXOPOULOS ILIAS



Title		PYTHAGORAS - CODEC	
Size	Number	Revision	
A3	SCH-00-003/002-ILX	V0.81	
Date:	19/11/2008	Sheet	8/8
File:	C:\temp\...codecsch	Drawn By:	ALEXOPOULOS ILIAS



Title			
Pythagoras - CODEC Input Interface			
Size	Number	Revision	
B	SCH-00-003002-OLX	V0.81	
Date:	19/11/2008	Sheet	6of
File:	C:\temp\codinp.sch	Drawn By:	ALEXOPOULOS ILIAS



Title			PYTHAGORAS LEV2 : CONNECTOR INTERFACE		
Size	Number			Revision	
B	SCH-00-003/002-ILX			V0.1	
Date:	19/11/2008	Sheet	9of		
File:	C:\temp\conints.ch	Drawn By:	ALEXOPOULOS ILIAS		